

Fig. 1a

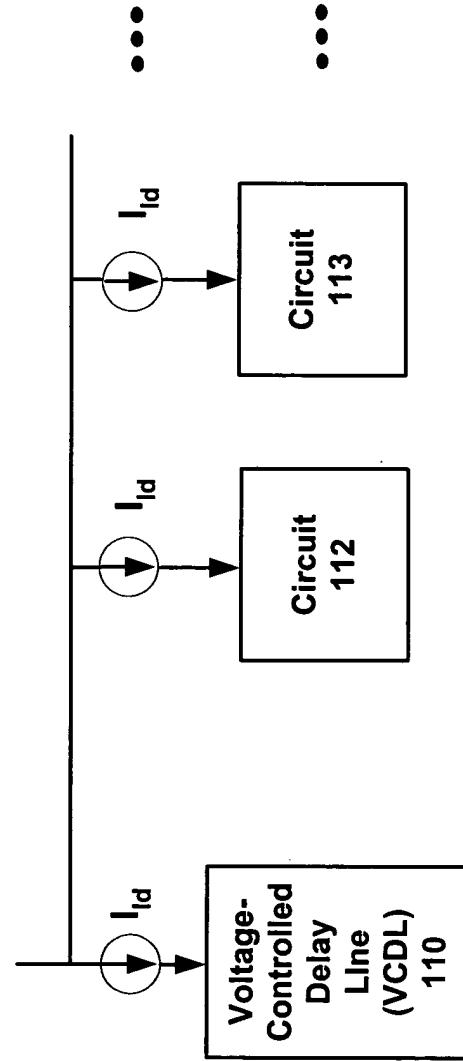


Fig. 1b

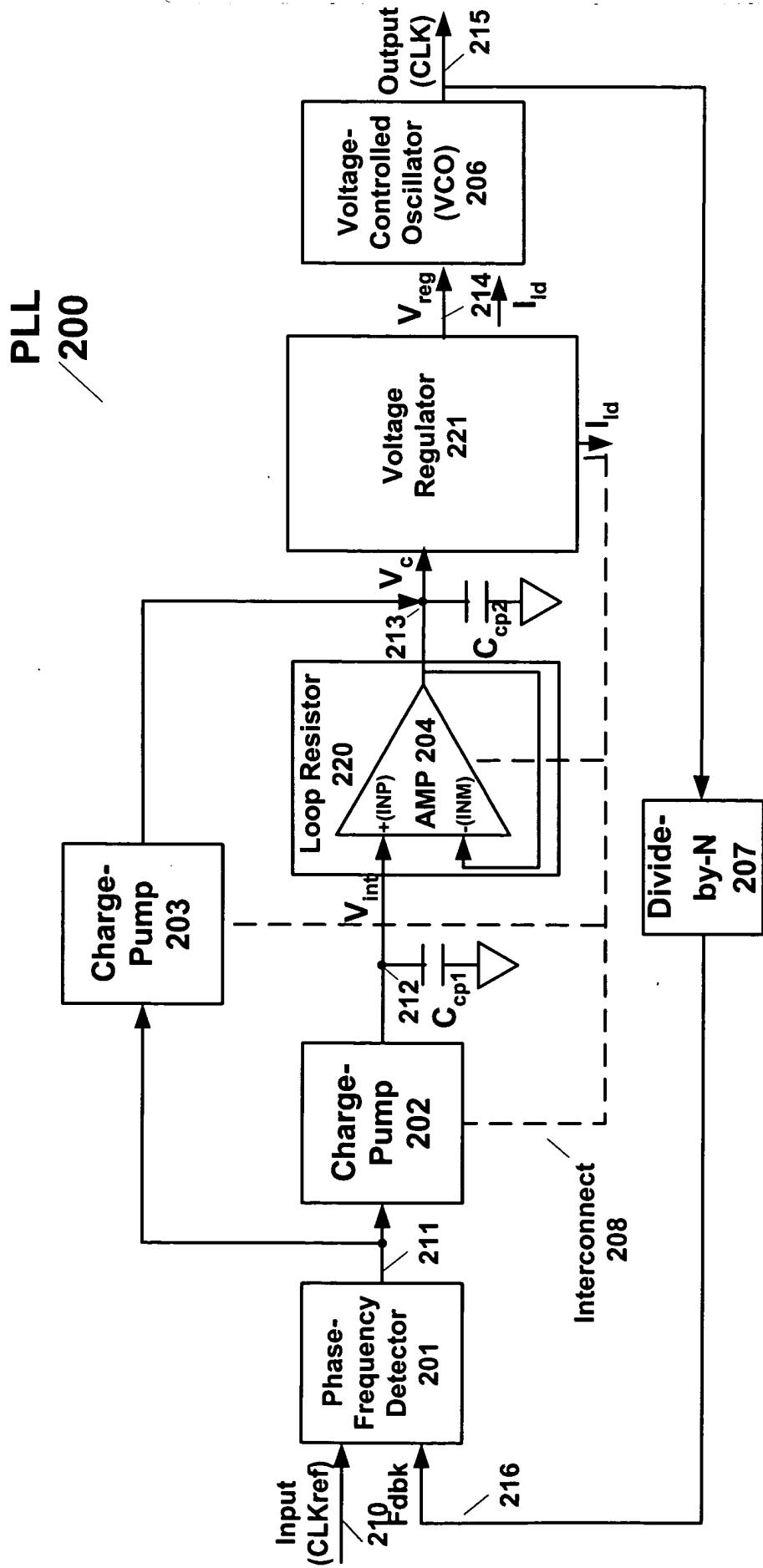


Fig. 2

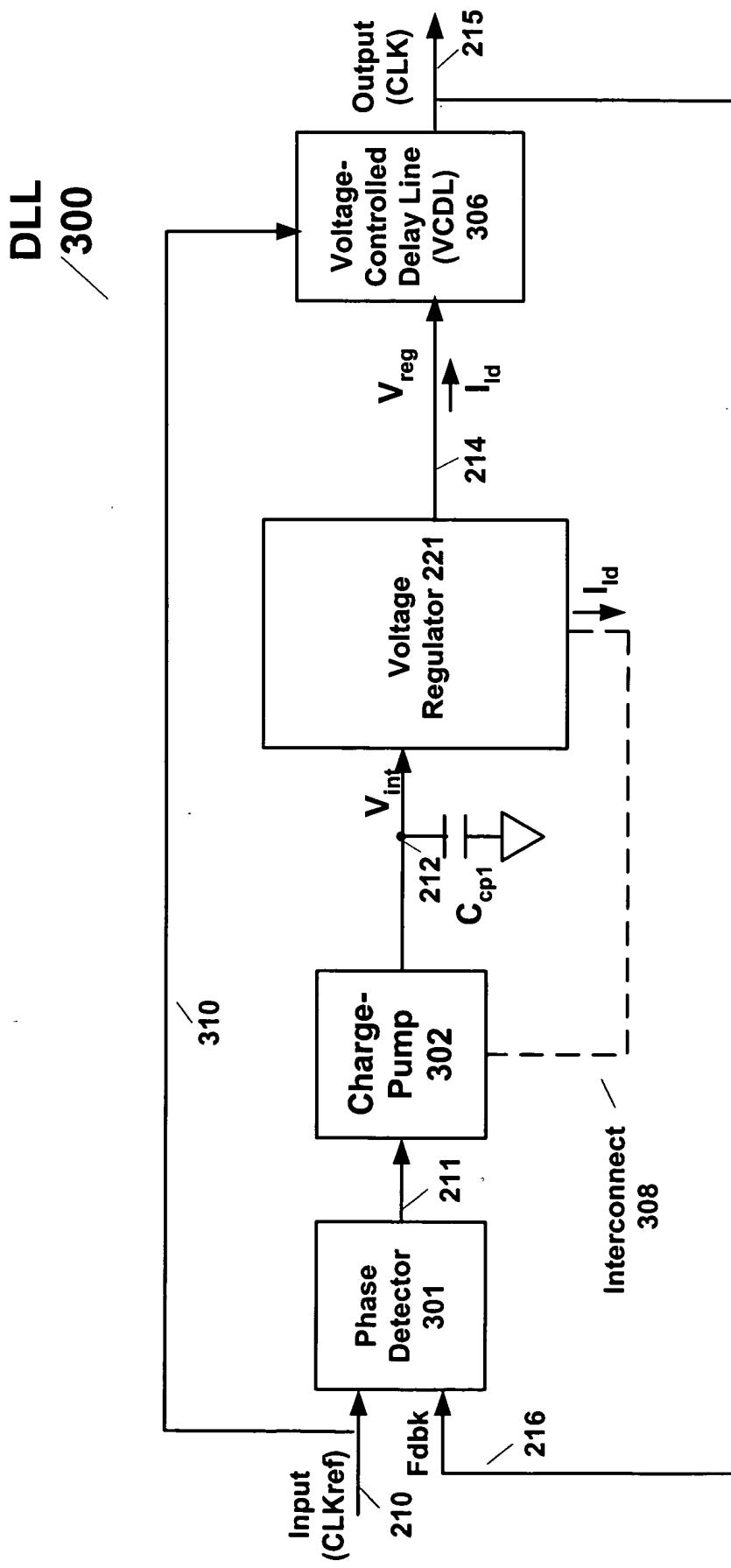


Fig. 3

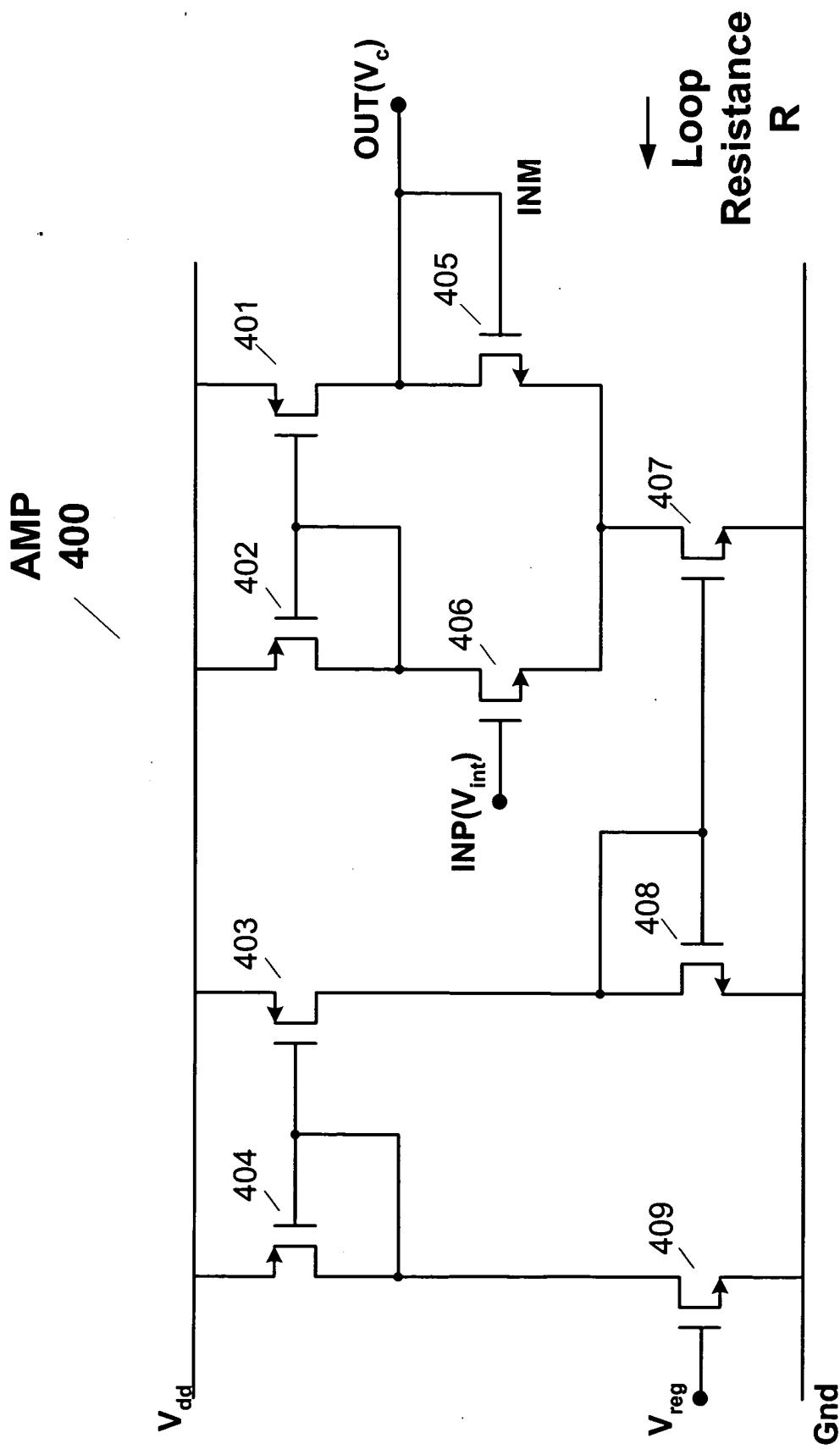


Fig. 4(Prior Art)

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Title: Regulated Adaptive-Bandwidth PLL/DLL Using Self-Biasing Current From A VCO/VCDL  
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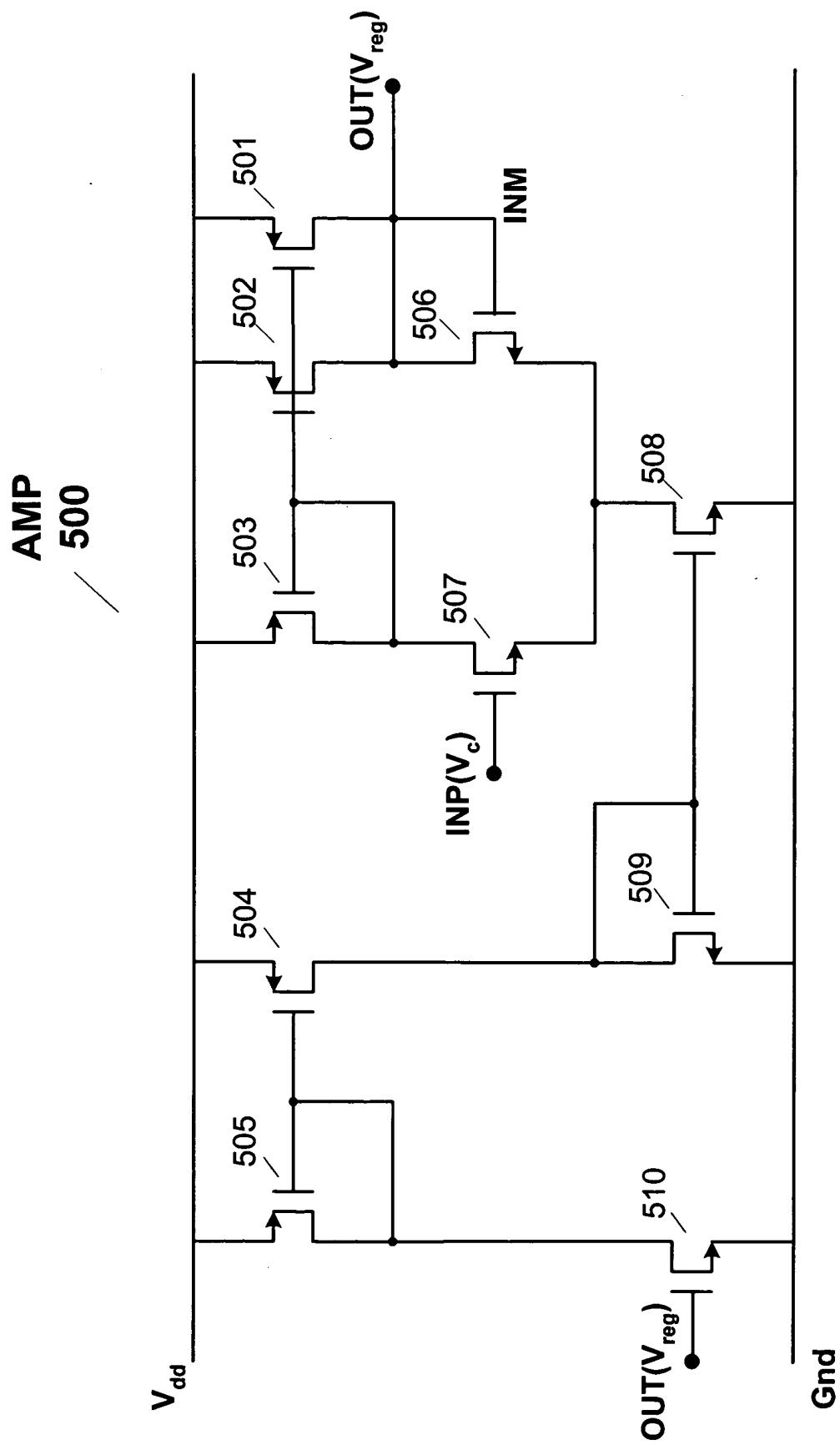


Fig. 5(Prior Art)

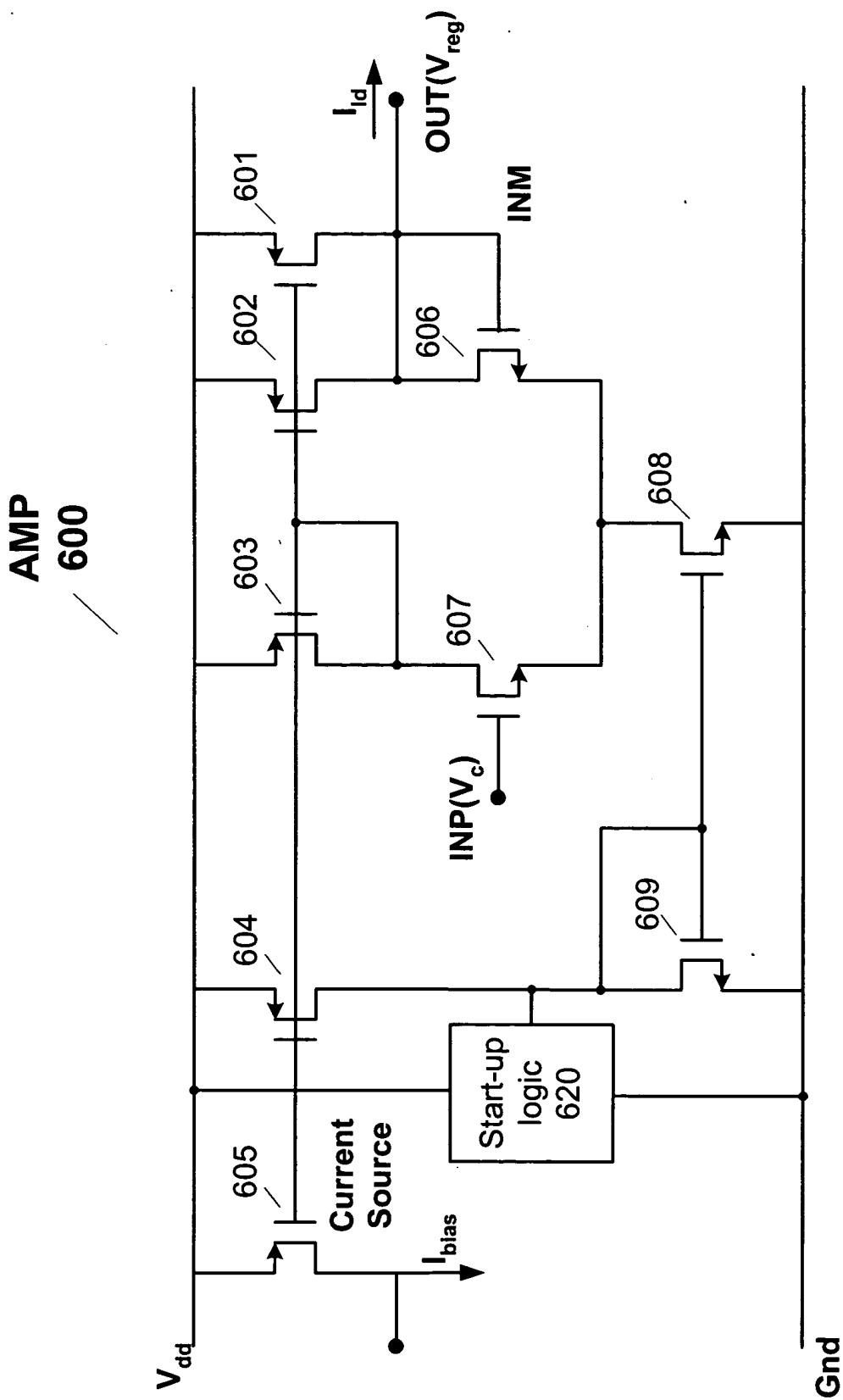


Fig. 6

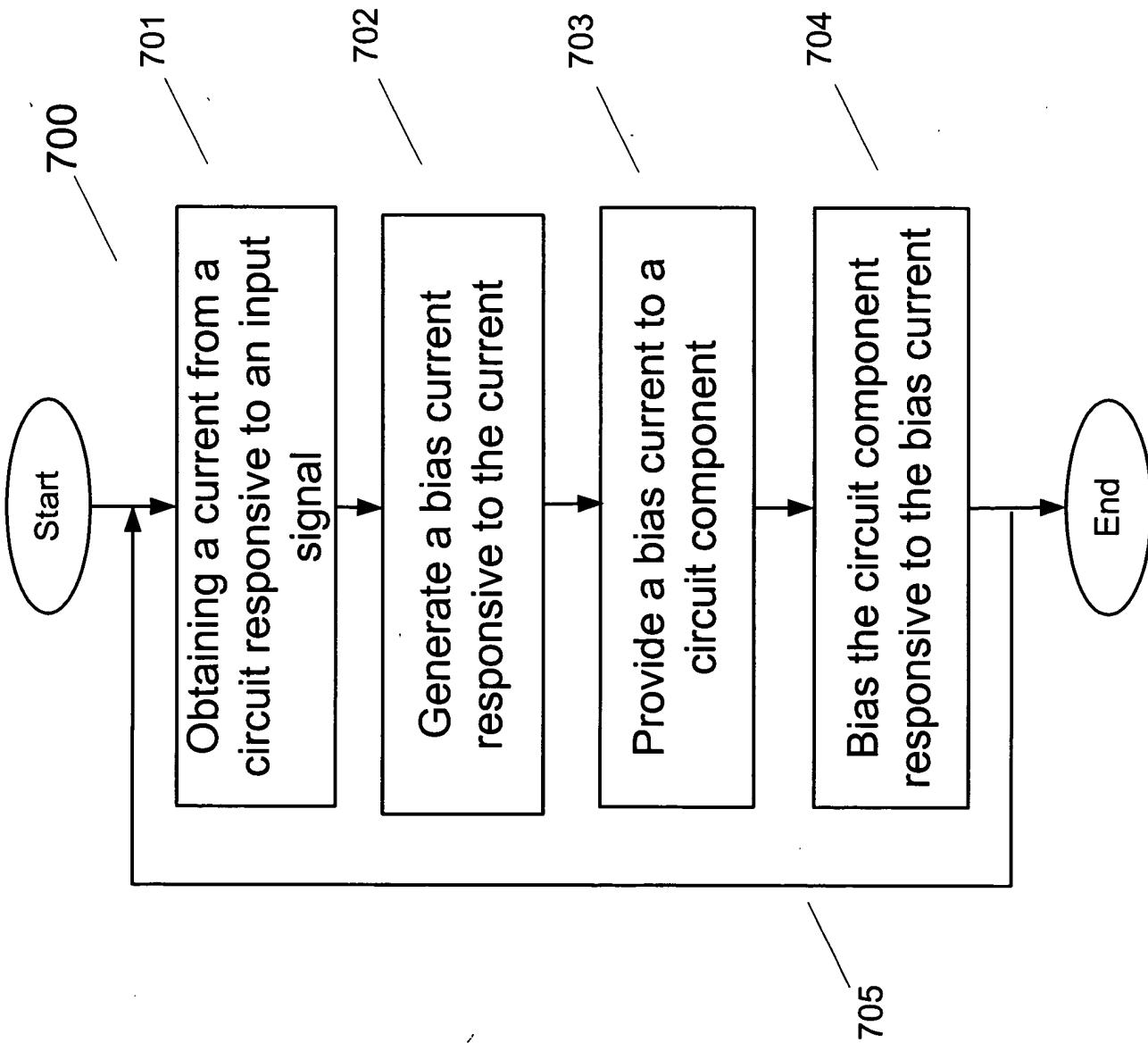


Fig. 7